

**SMD Power Inductor** VMPI73A-Series

**1. Features**

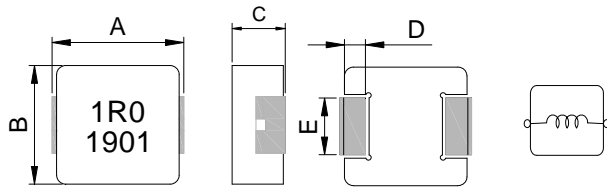
1. Shielded construction.
2. Capable of corresponding high frequency
3. Low loss realized with low DCR.
4. High performance (Isat) realized by metal dust core.
5. Ultra low buzz noise, due to composite construction.
6. 100% Lead(Pb)-Free and RoHS compliant.
7. Operating temperature: -40~+125°C (Including self-temperature rise)



**2. Applications**

1. DC/DC converters in distributed power systems.
2. DC/DC converter for Field Programmable Gate Array(FPGA).
3. Battery powered devices.
4. Thin type on-board power supply module for exchanger.
5. VRM for server.
6. High current, low profile POL converters.
7. PDA/notebook/desktop/server and battery powered devices.

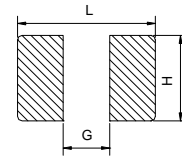
**3. Dimensions**



Series	A	B	C	D	E
VMPI73A	7.1±0.3	6.6±0.2	2.8±0.2	1.6±0.3	3.0±0.2

Unit:mm

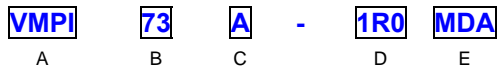
**Recommend PC Board Pattern**



L(mm)	G(mm)	H(mm)
8.0	3.7	3.4

Note: 1. The above PCB layout reference only.  
 2. Recommend solder paste thickness at 0.15mm and above.

**4. Part Numbering**



- A: Series
  - B: Dimension
  - C: Type
  - D: Inductance
  - E: Inductance Tolerance
- BxC
  - Standard.
  - 1R0=1.0uH
  - K=±10%, L=±15%, M=±20%, N=±25%, Y=±30%
  - Marking: Black.1R0 and 1901(19 YY, 01 WW, follow production date).

## 5. Specification

Part Number	Inductance L0 A(uH) ±20%	Heat Rating Current DC I rms ( A )Typ	Saturation Current DC I sat ( A )Typ	DCR (mΩ)Typ	DCR (mΩ)Max
VMPI73A-R22MDA	0.22	23	34	2.0	2.5
VMPI73A-R47MDA	0.47	18	20	3.4	4.0
VMPI73A-R68MDA	0.68	16	17	4.7	5.3
VMPI73A-1R0MDA	1.00	12	15	6.7	7.4
VMPI73A-2R2MDA	2.20	8	10	13.5	15
VMPI73A-3R3MDA	3.30	6.5	9.5	19	22
VMPI73A-4R7MDA	4.70	5.5	6.5	28	33
VMPI73A-100MDA	10.0	4.0	5.5	62	68
VMPI73A-220MDA	22.0	2.5	3.0	150	190

Note:

1. Test frequency : Ls : 100KHz /1.0V.
2. All test data referenced to 25°C ambient.
3. Testing Instrument(or equ) : L: HP4284A,CH11025,CH3302,CH1320,CH1320S LCR METER / Rdc:CH16502,Agilent33420A MICRO OHMMETER.
4. Heat Rated Current (I rms) will cause the coil temperature rise approximately ΔT of 40°C
5. Saturation Current (Isat) will cause L0 to drop approximately 30%.
6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions.Circuit design,component,PCB trace size and thickness,airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
7. Special inquiries besides the above common used types can be met on your requirement.

### 6. Typical Performance Curves

